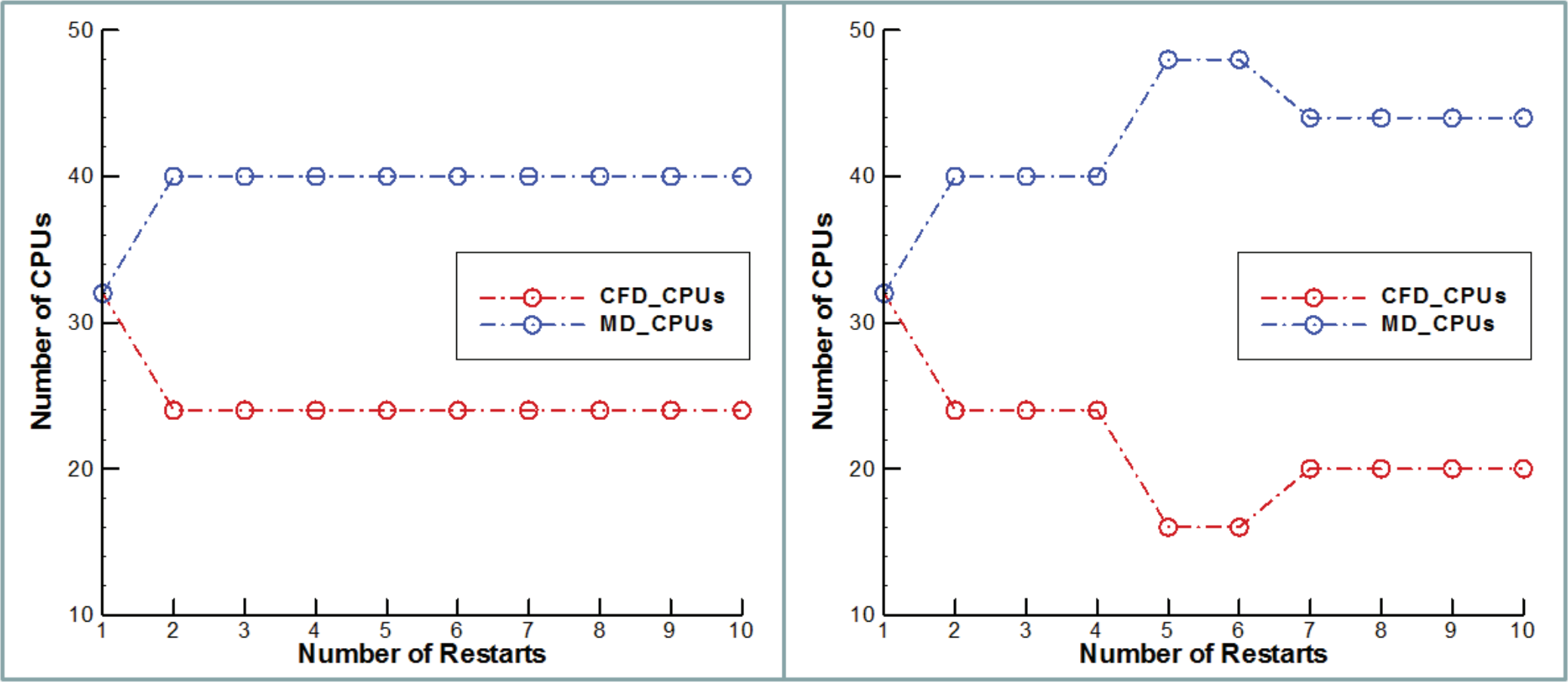
50
40
30
20
10
Number of CPUs
1
2
3
4
5
6
7
8
9
10
Number of Restarts
CFD_CPUs
MD_CPUs
50
40
30
20
10
Number of CPUs
1
2
3
4
5
6
7
8
9
10
Number of Restarts
CFD_CPUs
MD_CPUs